

FIG. 1

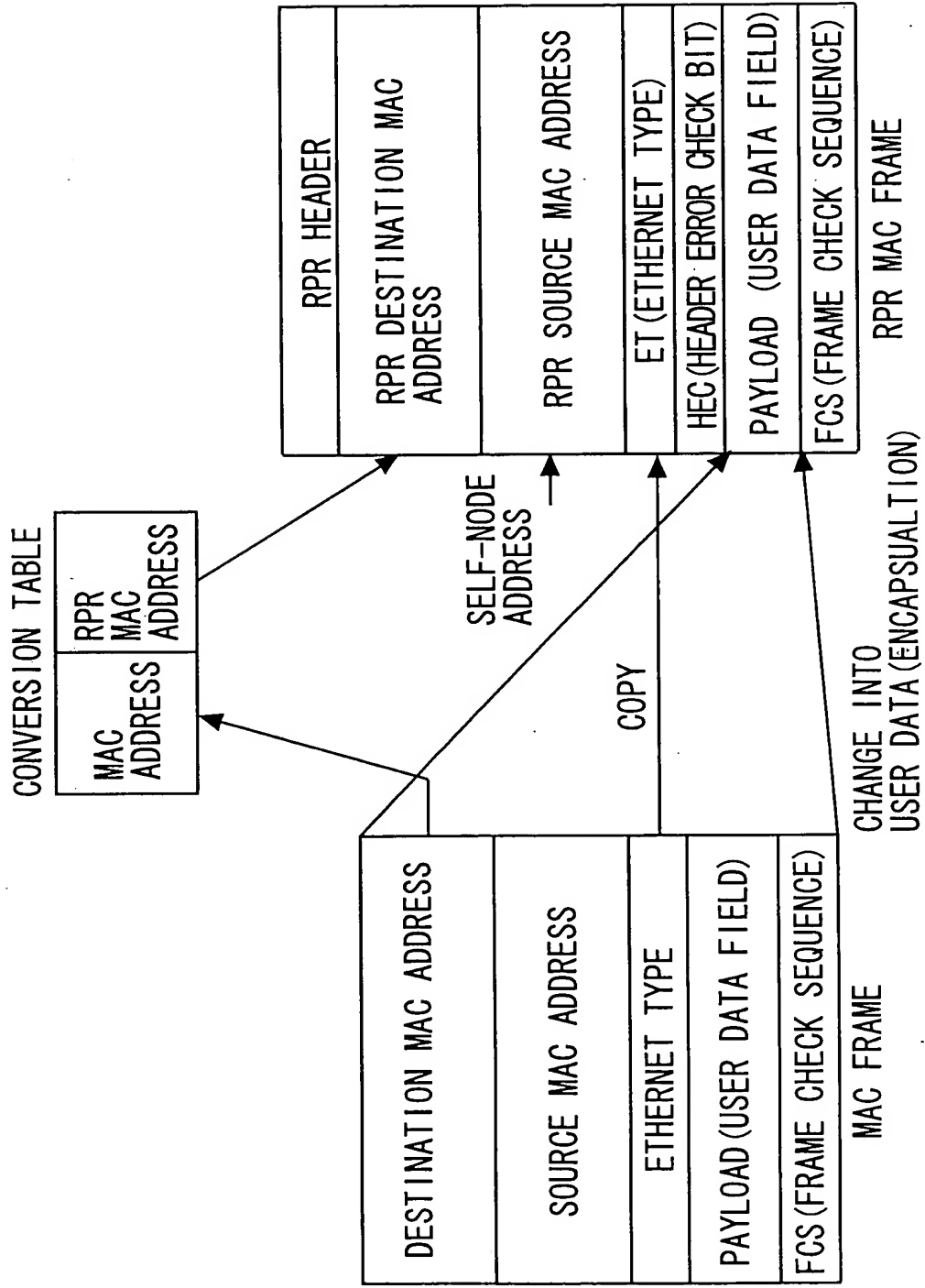


FIG. 2

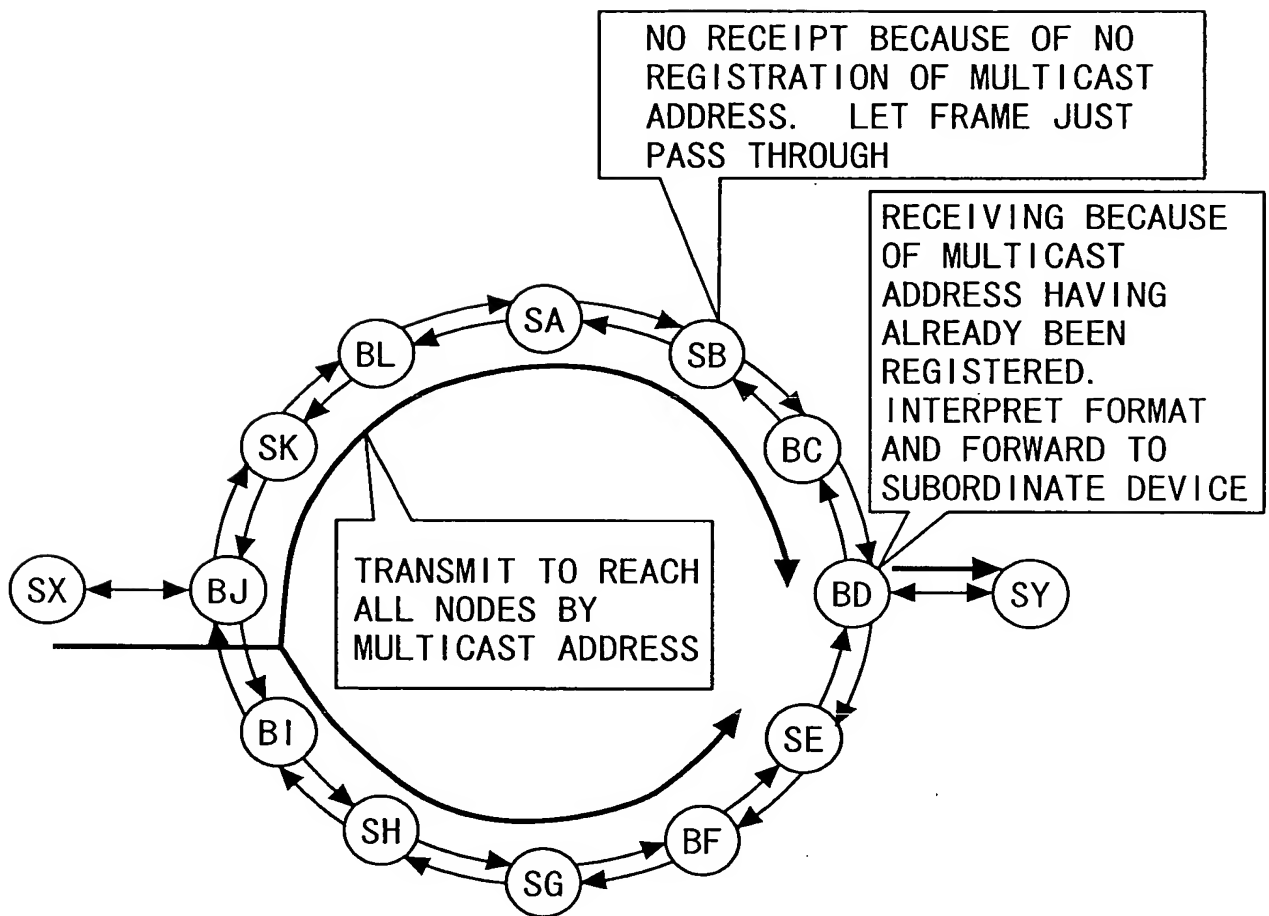


FIG. 3

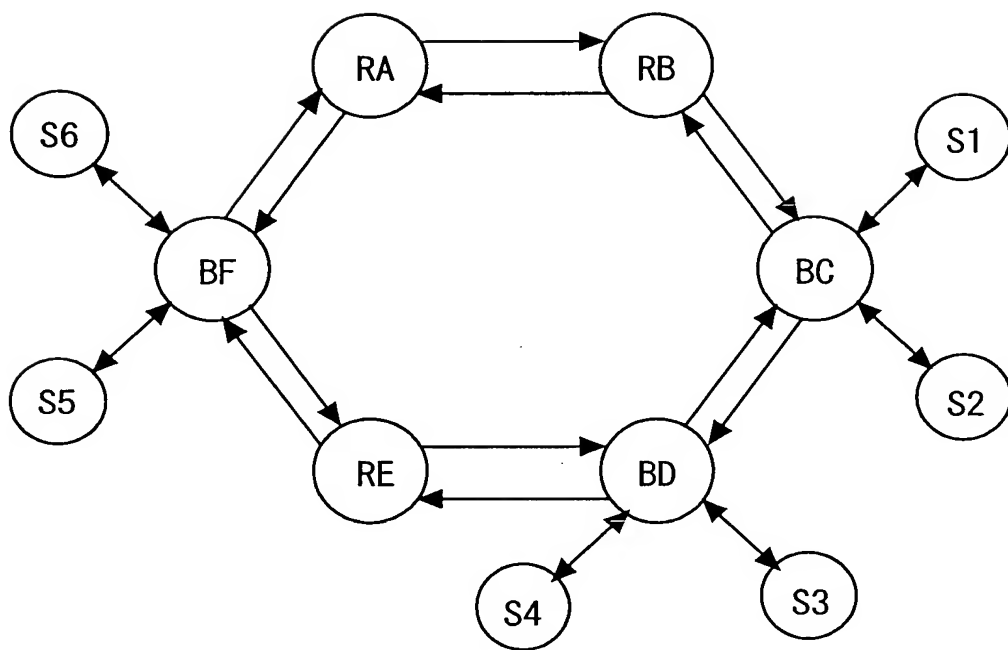


FIG. 4

DEVICE NAME	TYPE	IP ADDRESS	MAC ADDRESS	DEVICE NAME	TYPE	IP ADDRESS	MAC ADDRESS
RA	ROUTER (RPR NODE)	10.1.0.1	MRA	S1	STATION	10.1.0.10	MS1
RB	ROUTER (RPR NODE)	10.1.0.2	MRB	S2	STATION	10.1.0.11	MS2
BC	BRIDGE (RPR NODE)	—	MBC	S3	STATION	10.1.0.12	MS3
BD	BRIDGE (RPR NODE)	—	MBD	S4	STATION	10.1.0.13	MS4
RE	ROUTER (RPR NODE)	10.1.0.3	MRE	S5	STATION	10.1.0.14	MS5
BF	BRIDGE (RPR NODE)	—	MBF	S6	STATION	10.1.0.15	MS6

FIG. 5A

1) RA

NODE NAME	MAC ADDRESS	OUTER		INNER	
		TTL	STATUS	TTL	STATUS
RA	MRA	—	—	—	—
RB	MRB	1	IDLE	5	IDLE
RG	MBC	2	IDLE	4	IDLE
BD	MBD	3	IDLE	3	IDLE
RE	MRE	4	IDLE	2	IDLE
BF	MBF	5	IDLE	1	IDLE

FIG. 5B

2) RB

NODE NAME	MAC ADDRESS	OUTER		INNER	
		TTL	STATUS	TTL	STATUS
RB	MRB	—	—	—	—
BC	MBC	1	IDLE	5	IDLE
BD	MBD	2	IDLE	4	IDLE
RE	MRE	3	IDLE	3	IDLE
BF	MBF	4	IDLE	2	IDLE
RA	MRA	5	IDLE	1	IDLE

FIG. 5C

3) BC

NODE NAME	MAC ADDRESS	OUTER		INNER	
		TTL	STATUS	TTL	STATUS
BC	MBC	—	—	—	—
BD	MBD	1	IDLE	5	IDLE
RE	MRE	2	IDLE	4	IDLE
BF	MBF	3	IDLE	3	IDLE
RA	MRA	4	IDLE	2	IDLE
RB	MRB	5	IDLE	1	IDLE

FIG. 5D

4) BD

NODE NAME	MAC ADDRESS	OUTER		INNER	
		TTL	STATUS	TTL	STATUS
BD	MBD	—	—	—	—
RE	MRE	1	IDLE	5	IDLE
BF	MBF	2	IDLE	4	IDLE
RA	MRA	3	IDLE	3	IDLE
RB	MRB	4	IDLE	2	IDLE
BC	MBC	5	IDLE	1	IDLE

FIG. 5E

5) RE

NODE NAME	MAC ADDRESS	OUTER		INNER	
		TTL	STATUS	TTL	STATUS
RE	MRE	—	—	—	—
BF	MBF	1	IDLE	5	IDLE
RA	MRA	2	IDLE	4	IDLE
RB	MRB	3	IDLE	3	IDLE
BC	MBC	4	IDLE	2	IDLE
BD	MBD	5	IDLE	1	IDLE

FIG. 5G

6) BF

NODE NAME	MAC ADDRESS	OUTER		INNER	
		TTL	STATUS	TTL	STATUS
BF	MBF	—	—	—	—
RA	MRA	1	IDLE	5	IDLE
RB	MRB	2	IDLE	4	IDLE
BC	MBC	3	IDLE	3	IDLE
BD	MBD	4	IDLE	2	IDLE
RE	MRE	5	IDLE	1	IDLE

FIG. 6A

DA=BC
SA=MS1
PT=0x0806
ARP HEADER (ARP REQUEST)
SA=MS1
SIP=10. 1. 0. 10
DA=NULL
DIP=10. 1. 0. 15
FCS

ORIGINAL ARP
PACKET TO BE
TRANSMITTED BY S1

FIG. 6B

RPR HEADER
DA=BC
SA=MS1
PT=0x0806
HEC
ARP HEADER (ARP REQUEST)
SA=MS1
SIP=10. 1. 0. 10
DA=NULL
DIP=10. 1. 0. 15
FCS

RPR-ENCAPSULATED
ARP PACKET TO BE
TRANSMITTED BY BC

FIG. 6C

DA=MS1
SA=MS6
PT=0x0806
ARP HEADER (ARP RESPONSE)
SA=MS6
SIP=10. 1. 0. 15
DA=MS1
DIP=10. 1. 0. 10
FCS

ARP RESPONSE
PACKET TO BE
TRANSMITTED BY S6

FIG. 6D

RPR HEADER
DA=MC
SA=MBF
PT=0x0806
HEC
DA=MS1
SA=MS6
PT=0x0806
ARP HEADER (ARP RESPONSE)
SA=MS6
SIP=10. 1. 0. 15
DA=MS1
DIP=10. 1. 0. 10
FCS

ENCAPSUALTED ARP
RESPONSE PACKET TO
BE TRANSMITTED BY BF

FIG. 7A

DA=MS6
SA=MS1
PT=0x0800
IP HEAD
IDA=10. 1. 0. 15
ISA=10. 1. 0. 10
PAYLOAD
FCS

IP DATA PACKET
TO BE TRANSMITTED
BY S1

FIG. 7B

RPR HEADER
DA=MBF
SA=MBC
PT=0x0800
HEC
DA=MS6
SA=MS1
PT=0x0800
IP HEADER
IDA=10. 1. 0. 15
ISA=10. 1. 0. 10
PAYLOAD
FCS

ENCAPSUALTED IP
DATA PACKET TO
BE TRANSMITTED
BY BC

FIG. 8A

RPR HEADER
DA=BC
SA=MRA
PT=0x0806
HEC
ARP HEADER (ARP REQUEST)
SA=MRA
SIP=10. 1. 0. 1
DA=NULL
DIP=10. 1. 0. 12
FCS

ARP PACKET TO
BE TRANSMITTED
BY RA

FIG. 8B

DA=BC
SA=MRA
PT=0x0806
ARP HEADER (ARP REQUEST)
SA=MRA
SIP=10. 1. 0. 1
DA=NULL
DIP=10. 1. 0. 12
FCS

ARP REQUEST PACKET
TO BE TRANSMITTED
BY BD

FIG. 8C

DA=MRA
SA=MS3
PT=0x0806
ARP HEADER (ARP RESPONSE)
SA=MS3
SIP=10. 1. 0. 12
DA=MRA
DIP=10. 1. 0. 1
FCS

ARP RESPONSE PACKET
TO BE TRANSMITTED
BY S3

FIG. 8D

RPR HEADER
DA=MRA
SA=MS3
PT=0x0806
HEC
ARP HEADER (ARP RESPONSE)
SA=MS3
SIP=10. 1. 0. 12
DA=MRA
DIP=10. 1. 0. 1
FCS

ARP RESPONSE PACKET
TO BE TRANSMITTED
BY BD

FIG. 9A

RPR HEADER
DA=MBD
SA=MRA
PT=0x0800
HEC
DA=MS3
SA=MRA
PT=0x0800
IP HEAD
IDA=10. 1. 0. 12
ISA=10. 1. 0. 1
PAYLOAD
FCS

IP DATA PACKET
TO BE TRANSMITTED
BY RA

FIG. 9B

DA=MRA
SA=MS3
PT=0x0800
IP HEAD
IDA=10. 1. 0. 1
ISA=10. 1. 0. 12
PAYLOAD
FCS

IP DATA PACKET
TO BE TRANSMITTED
BY S3

FIG. 9C

RPR HEADER
DA=MRA
SA=MS3
PT=0x0800
HEC
IP HEAD
IDA=10. 1. 0. 1
ISA=10. 1. 0. 12
PAYLOAD
FCS

IP DATA PACKET
TO BE TRASLATED
AND TRANSMITTED
BY BD

FIG. 10

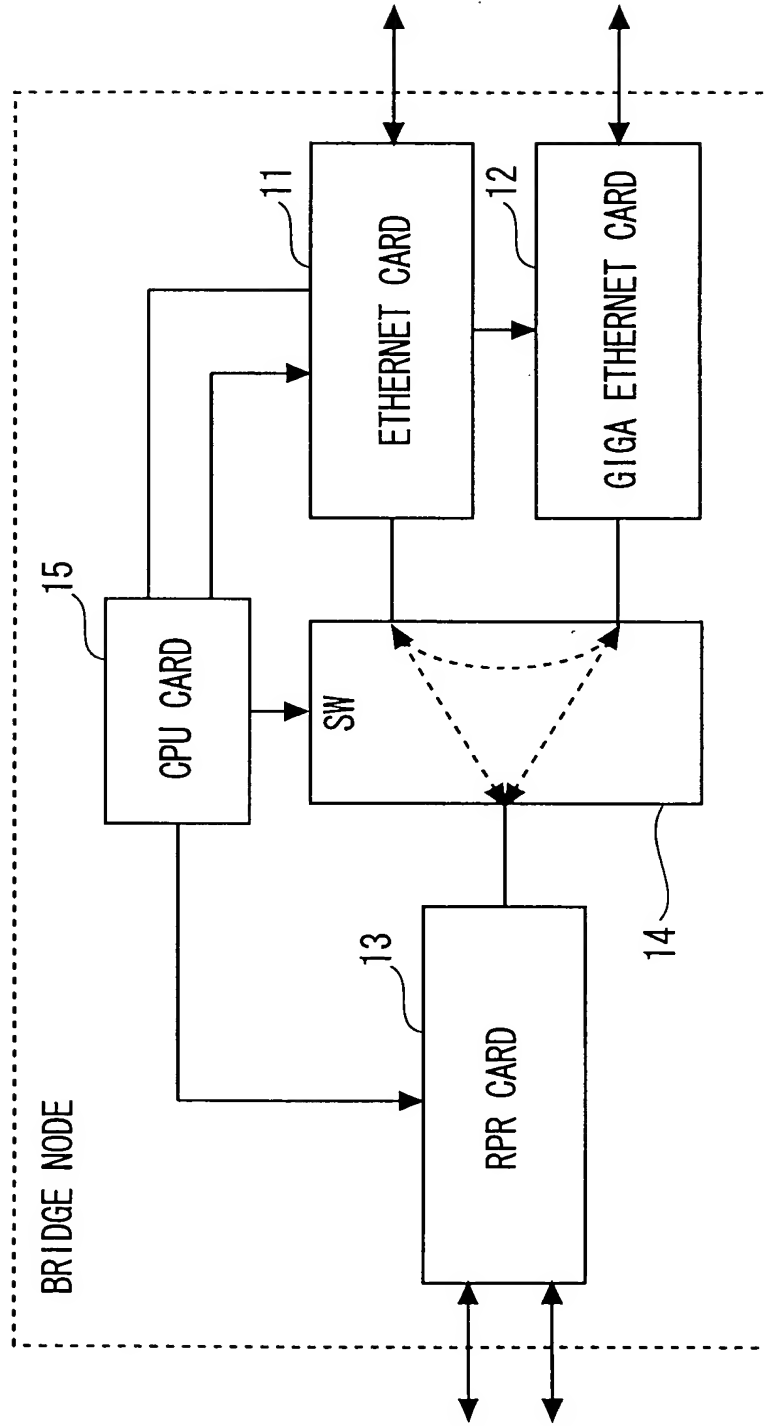


FIG. 11

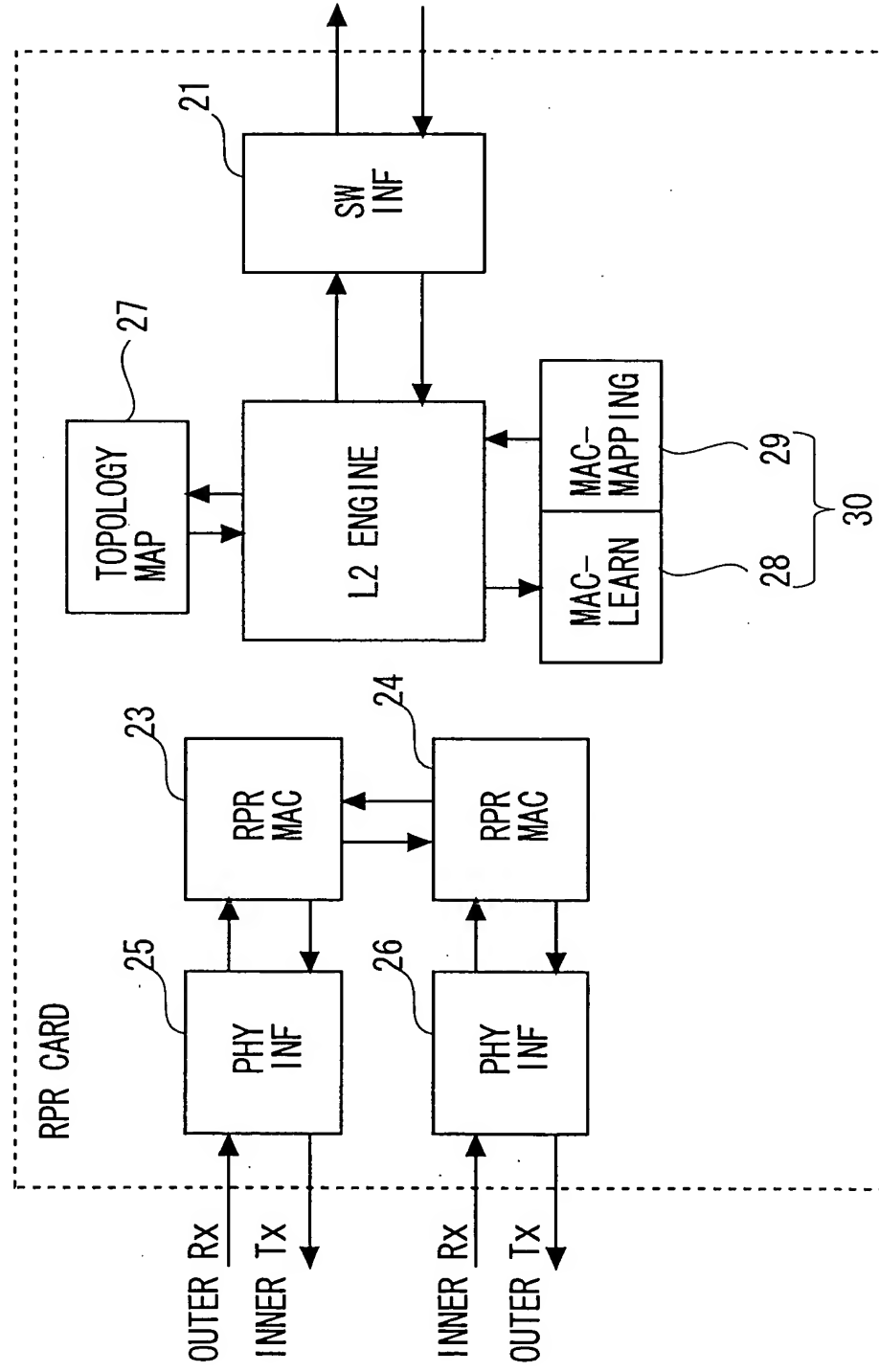


FIG. 12

30

DEVICE NAME	MAC ADDRESS	DEVICE NAME	MAC ADDRESS	PORT NUMBER
RA	MRA	—	—	0 (RPR)
RB	MRB	—	—	0 (RPR)
BC	MBC	S1	MS1	1 (ETHERNET)
		S2	MS2	2 (GETHERNET)
BD	MBD	S3	MS3	0 (RPR)
		S4	MS4	0 (RPR)
RE	MRE	—	—	0 (RPR)
BF	MBF	S5	MS5	0 (RPR)
		S6	MS6	0 (RPR)

FIG. 13

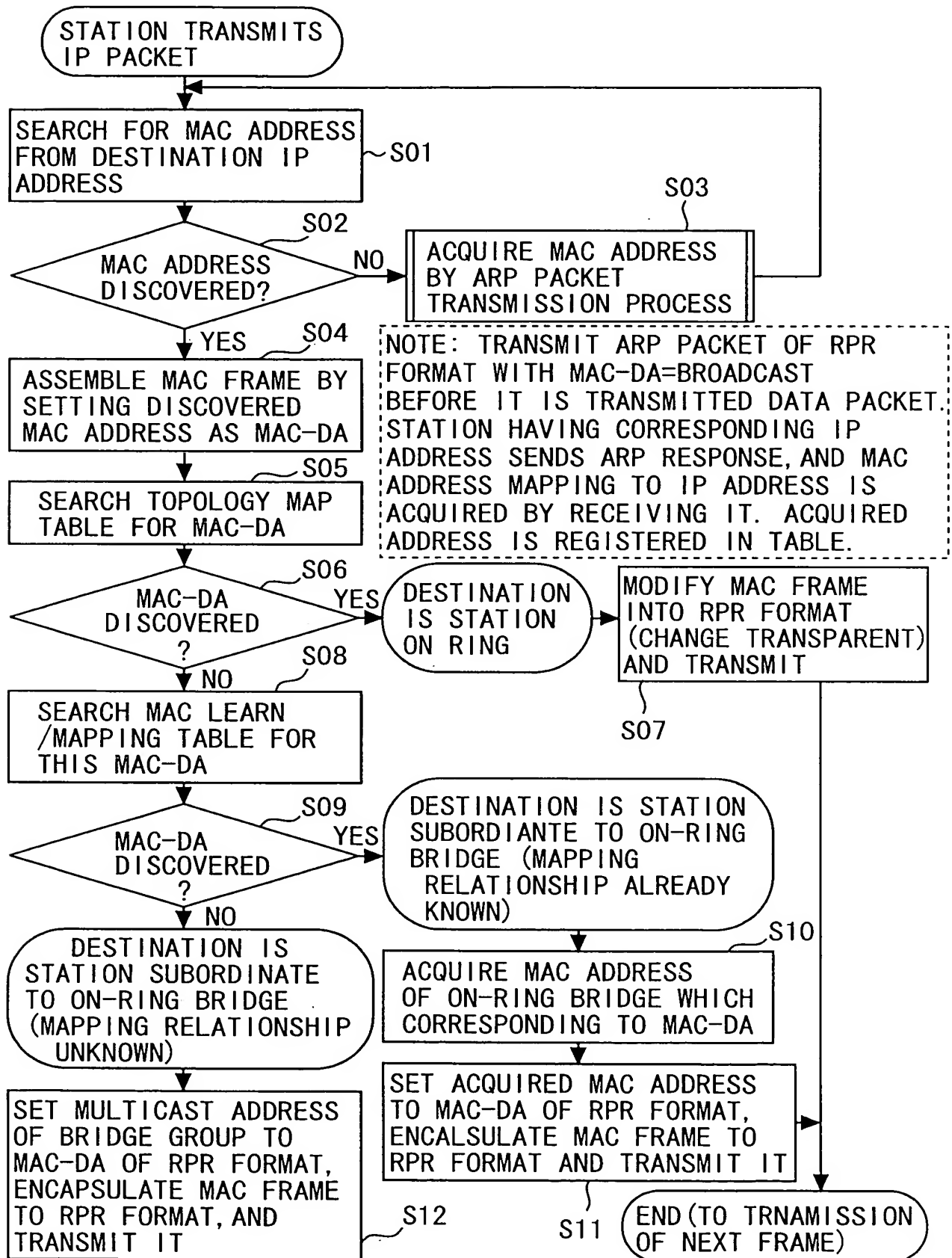


FIG. 14

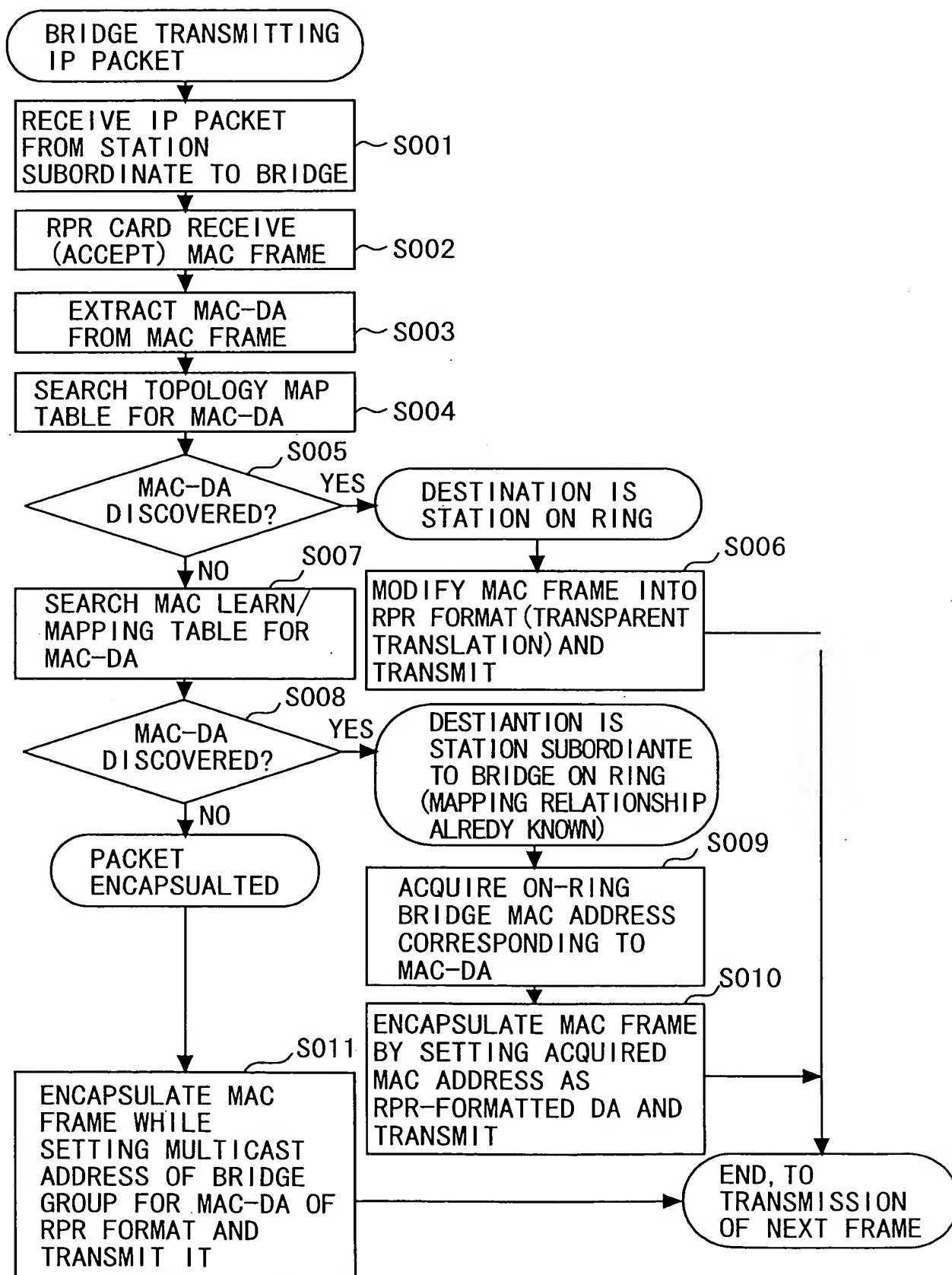


FIG. 15

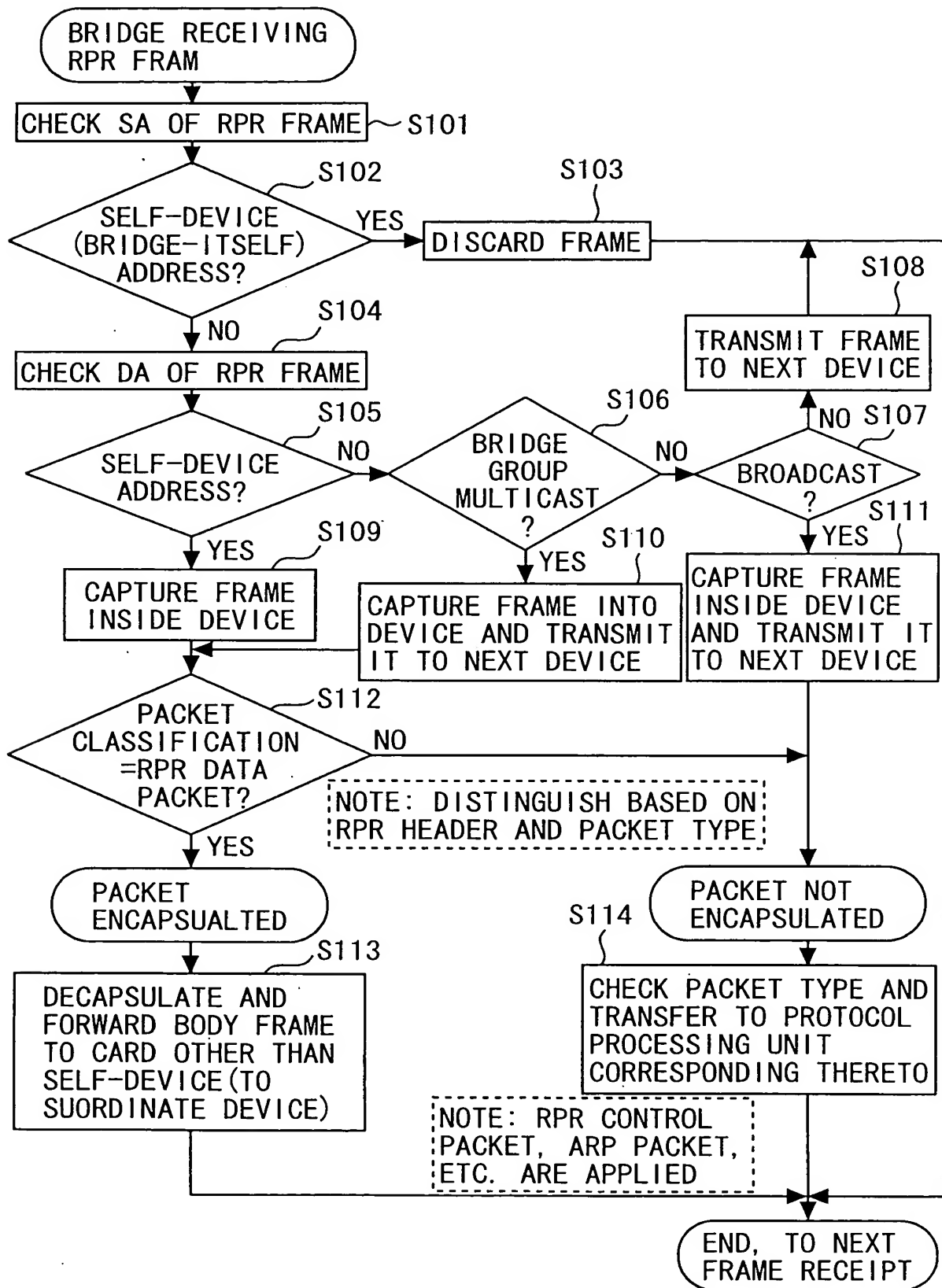


FIG. 16

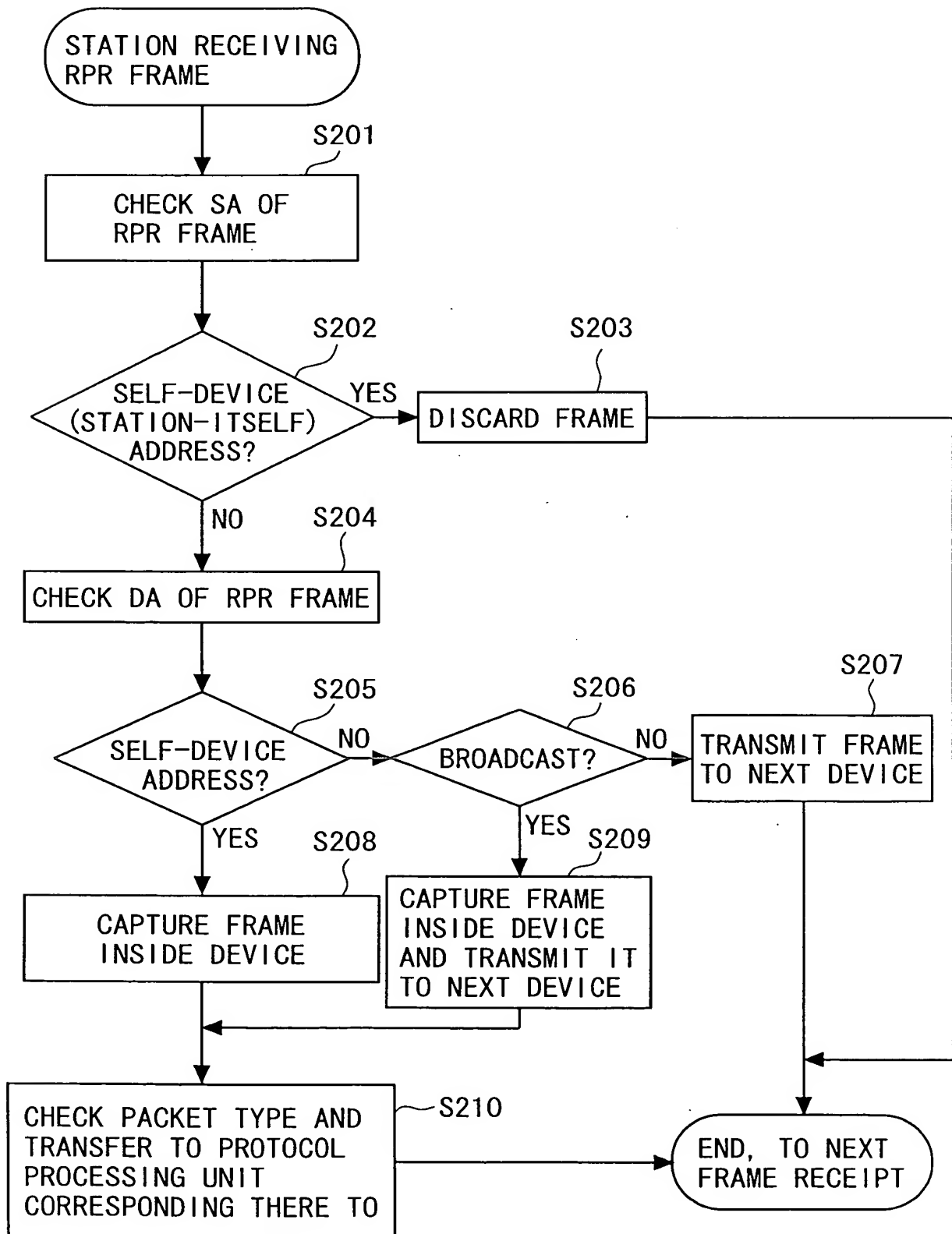


FIG. 17A

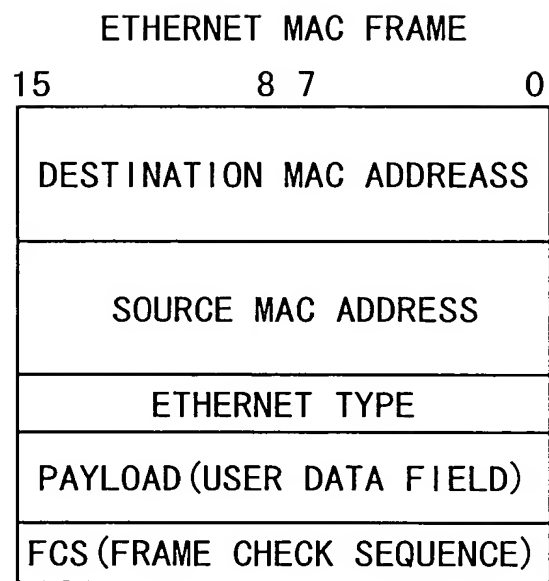
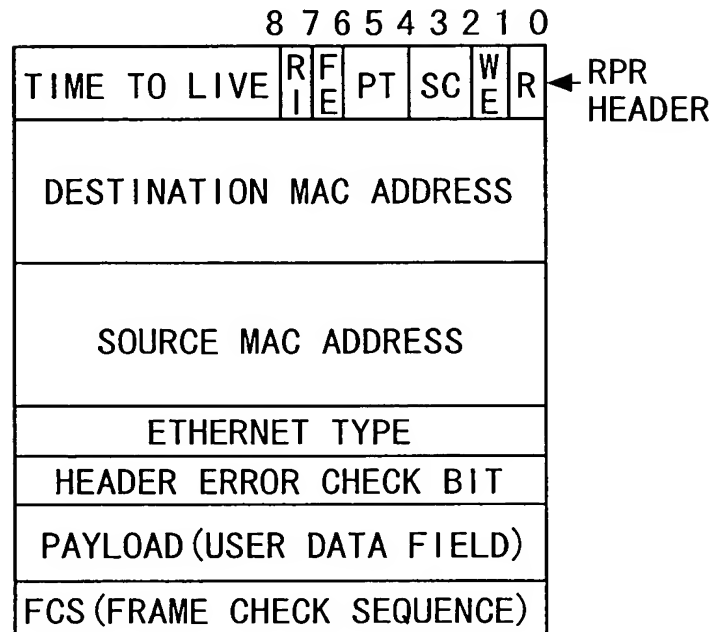


FIG. 17B

RRR MAC FRAME



NOTE: MEANINGS OF RESPECTIVE FIELDS OF RPR HEADER,
TTL: INDICATING TIME-TO-LIVE OF FRAME. ONE IS SUBTRACT
FROM TTL VALUE (-1) FOR EVERY 1-NODE PASSAGE
(DEFINITION OF NODE WILL BE GIVEN IN NEXT ITEM),
WHEN RESULT OF DECREASE (-1) BECOMES 0, FRAME
IS NOT FORWARDED TO NEXT NODE.

RI: ID OF RINGLET. RINGLET IS USED WHEN INDICATING ONE
OF BIDIRECTIONAL RINGS, AND THIS BIT INDICATES
WHICH RINGLET THIS FRAME ORIGINALLY EXISTS.
RINGLET 0 = 0, RINGLET 1 = 1.

FE: FE INDICATES WHETHER THIS PACKET IS FAIRNESS
CONTROL OBJECT OR NOT. FE = 0, THIS IS NOT FAIRNESS
OBJECT, FE = 1, THIS IS FAIRNESS OBJECT.

PT: PT DEFINES PACKET ATTRIBUTE. 00 = PROSPECTIVE
RESERVATION, 01 = RPR CONTROL PACKET, 10 = RPR FAIRNESS
PACKET, FE = 1, THIS IS FAIRNESS OBJECT.

SC: SERVICE CLASS. , 00 = CLASS C, 01 = CLASS B,
10 = CLASS A (SUBCLASS A1), 11 = CLASS A (SUBCLASS
A0, WE: LAPPING FUNCTION PROVIDED OR NON-PROVIDED 0 =
NO LAPPING FUNCTION, 1 = LAPPING FUNCTION PROVIDED.

R: PROSPECTIVE RESERVATION

FIG. 18

PRIOR ART

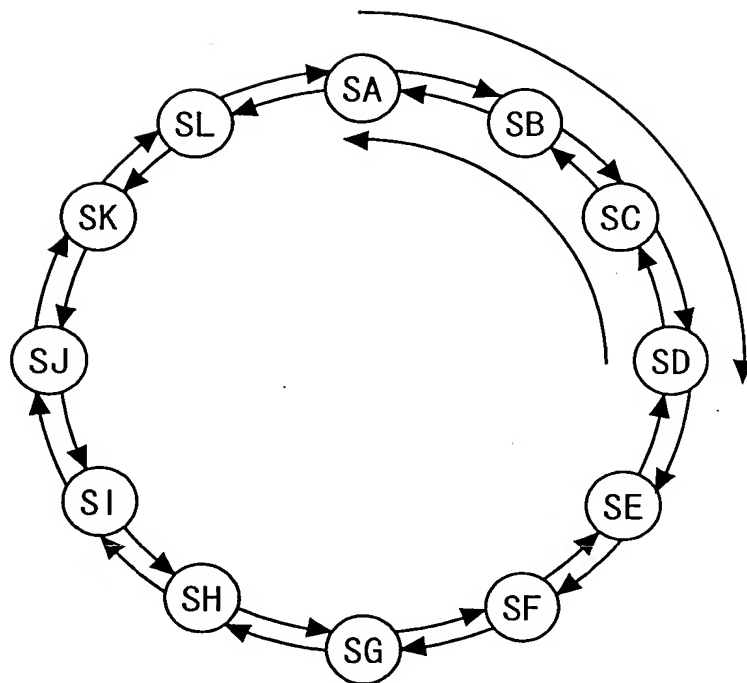


FIG. 19

PRIOR ART

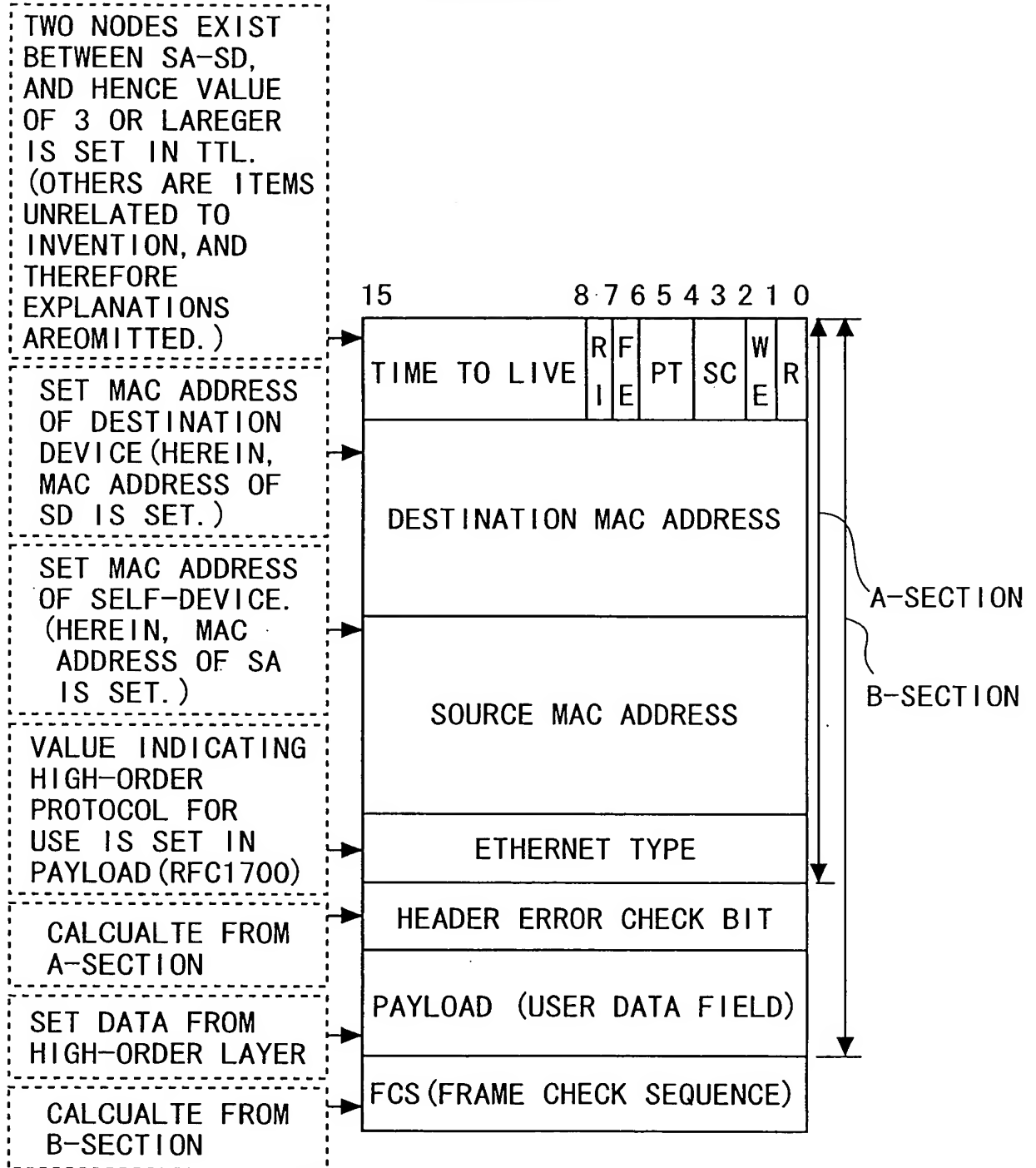


FIG. 20

PRIOR ART

NODE NAME	MAC ADDRESS	OUTER		INNER	
		TTL	STATUS	TTL	STATUS
SA	MSA	—	—	—	—
SB	MSB	1	IDLE	11	IDLE
SC	MSC	2	IDLE	10	IDLE
SD	MSD	3	IDLE	9	IDLE
SE	MSE	4	IDLE	8	IDLE
SF	MSF	5	IDLE	7	IDLE
SG	MSG	6	IDLE	6	IDLE
SH	MSH	7	IDLE	5	IDLE
SI	MSI	8	IDLE	4	IDLE
SJ	MSJ	9	IDLE	3	IDLE
SK	MSK	10	IDLE	2	IDLE
SL	MSL	11	IDLE	1	IDLE

FIG. 21

PRIOR ART

NODE NAME	MAC ADDRESS	OUTER		INNER	
		TTL	STATUS	TTL	STATUS
SA	MSA	—	—	—	—
SB	MSB	1	IDLE	11	IDLE
SC	MSC	2	IDLE	10	IDLE
SD	MSD	3	IDLE→BUSY	9	IDLE
SE	MSE	4	IDLE→BUSY	8	IDLE
SF	MSF	5	IDLE→BUSY	7	IDLE
SG	MSG	6	IDLE→BUSY	6	IDLE
SH	MSH	7	IDLE→BUSY	5	IDLE
SI	MSI	8	IDLE→BUSY	4	IDLE
SJ	MSJ	9	IDLE→BUSY	3	IDLE
SK	MSK	10	IDLE→BUSY	2	IDLE
SL	MSL	11	IDLE→BUSY	1	IDLE

FIG. 22

PRIOR ART

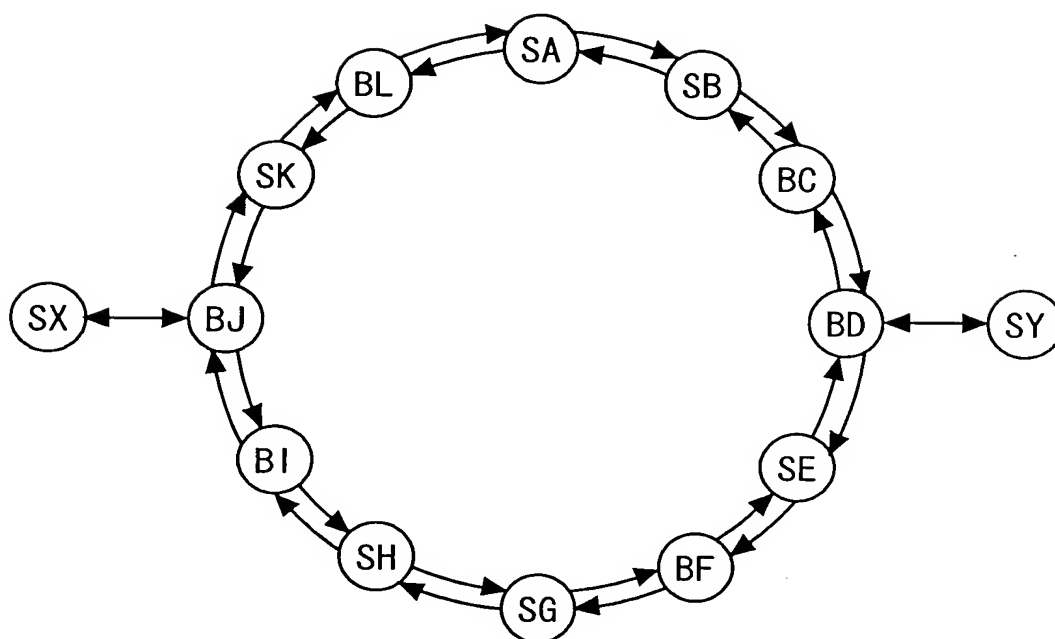


FIG. 23
PRIOR ART

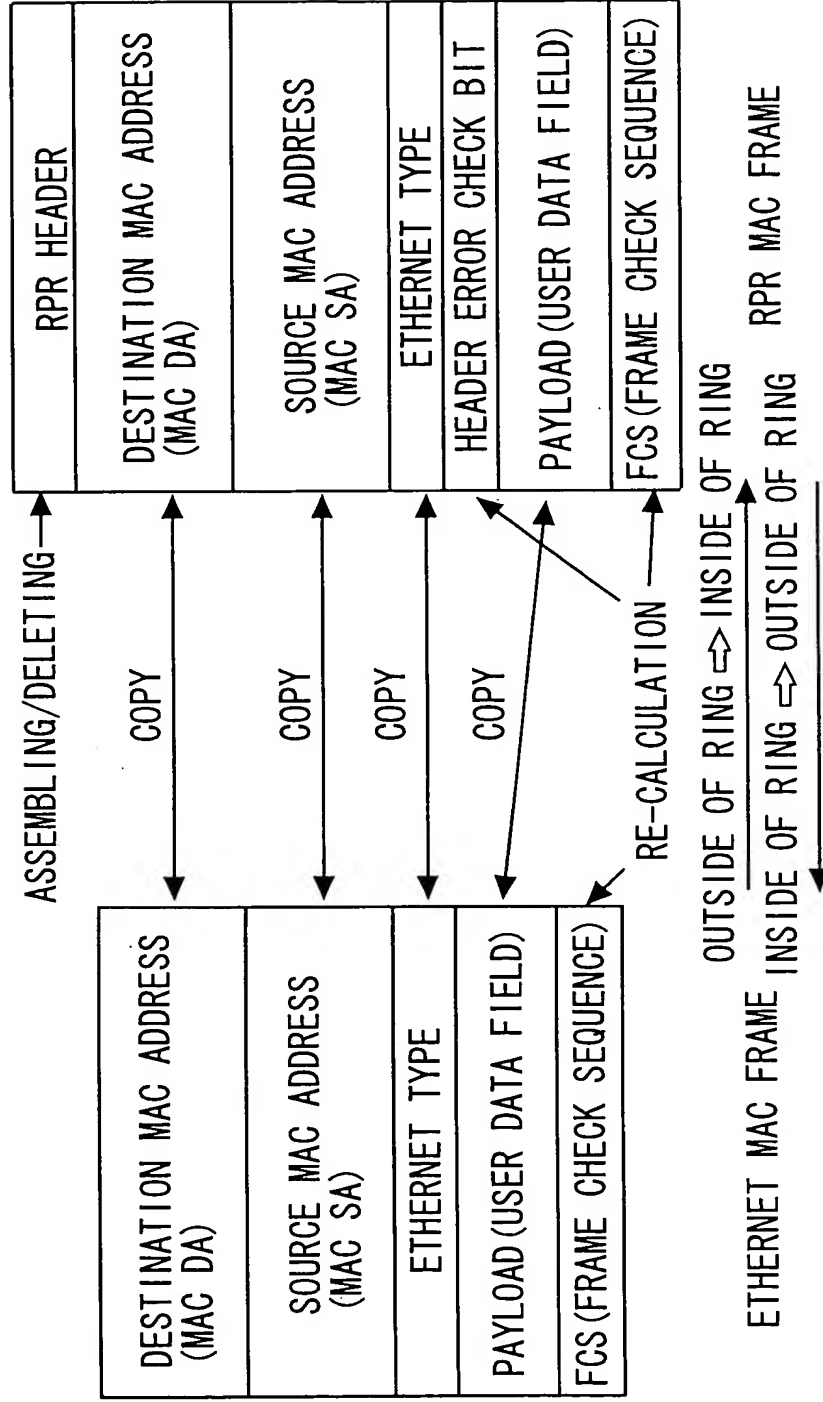


FIG. 24

PRIOR ART

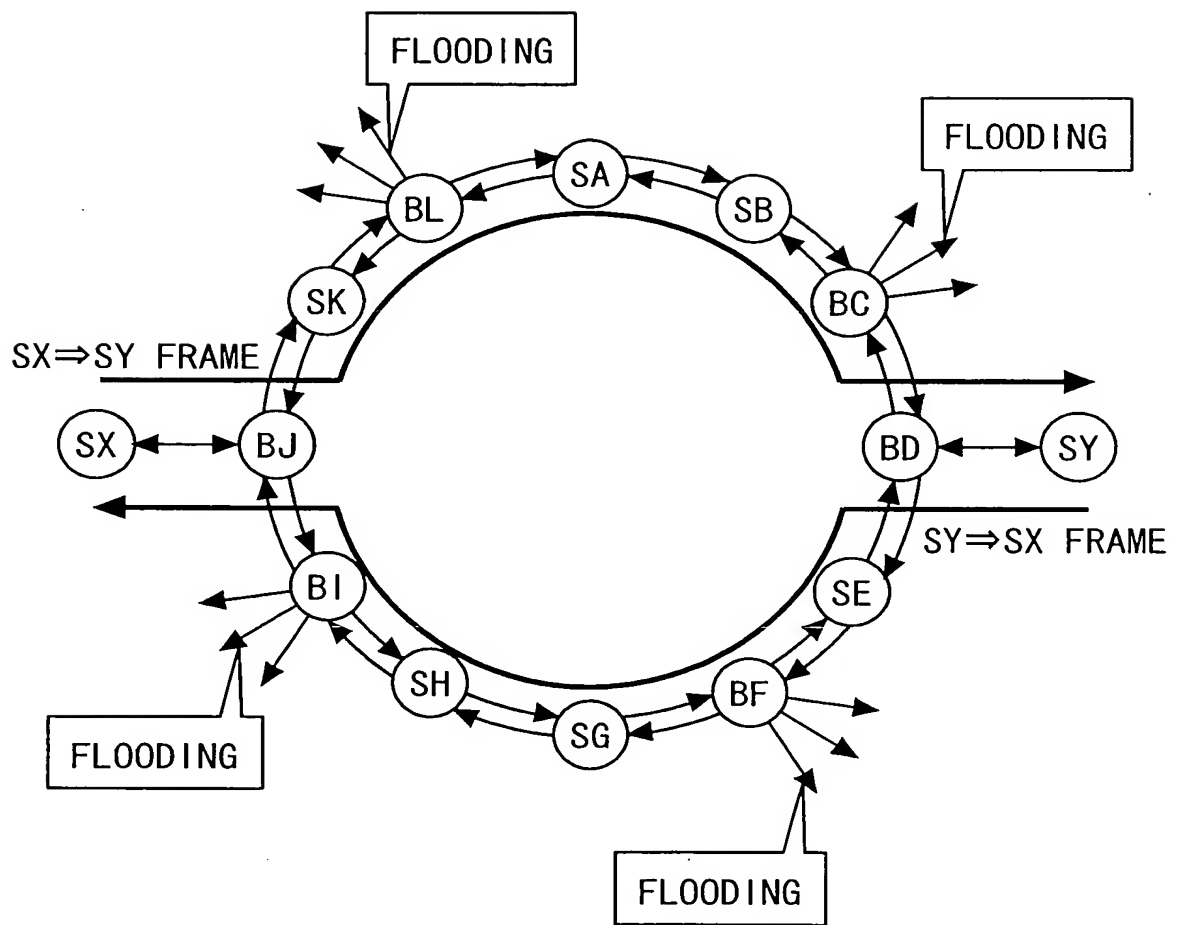


FIG. 25

PRIOR ART

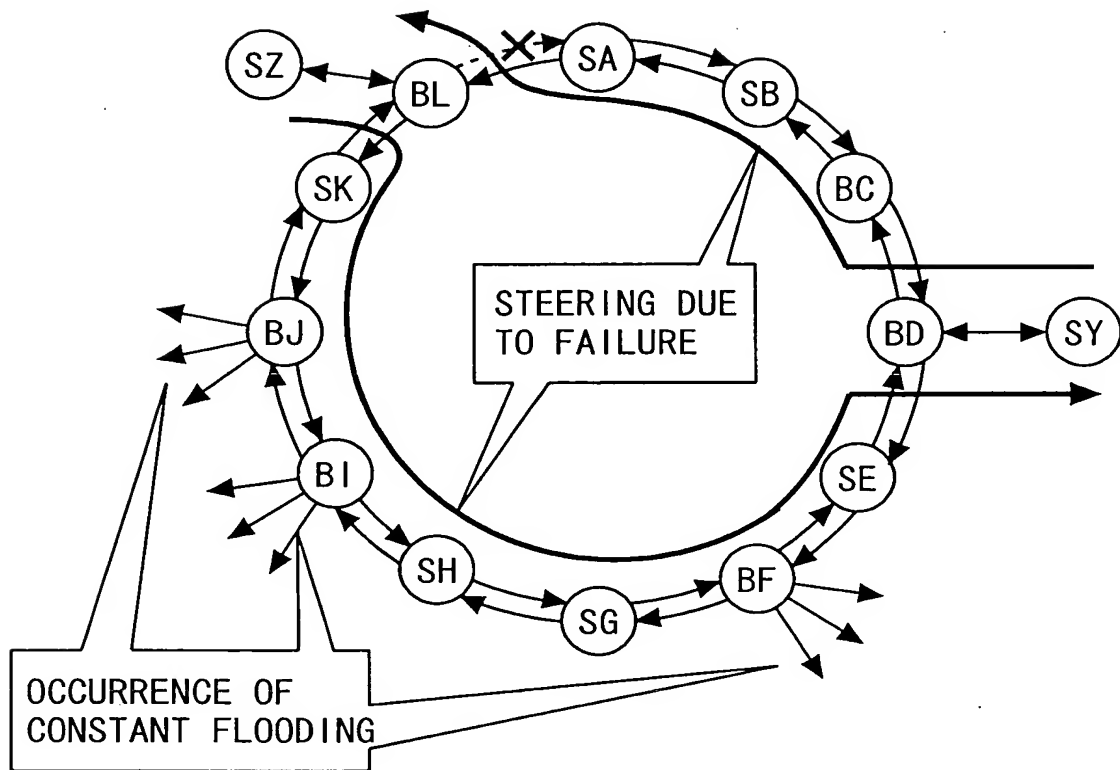


FIG. 26

PRIOR ART

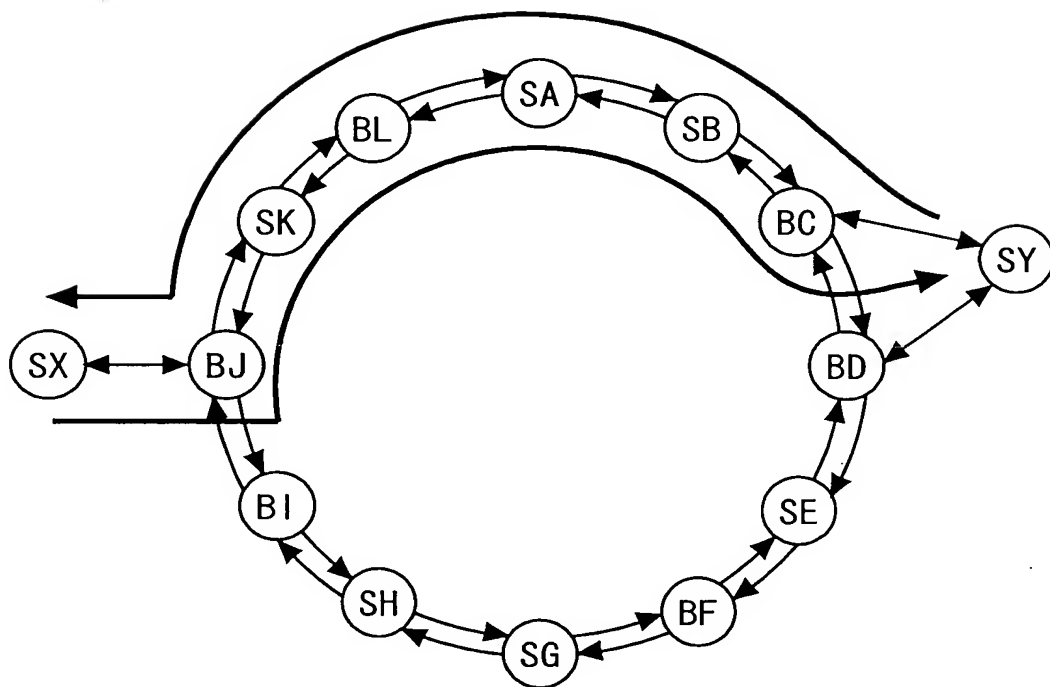


FIG. 27

PRIOR ART

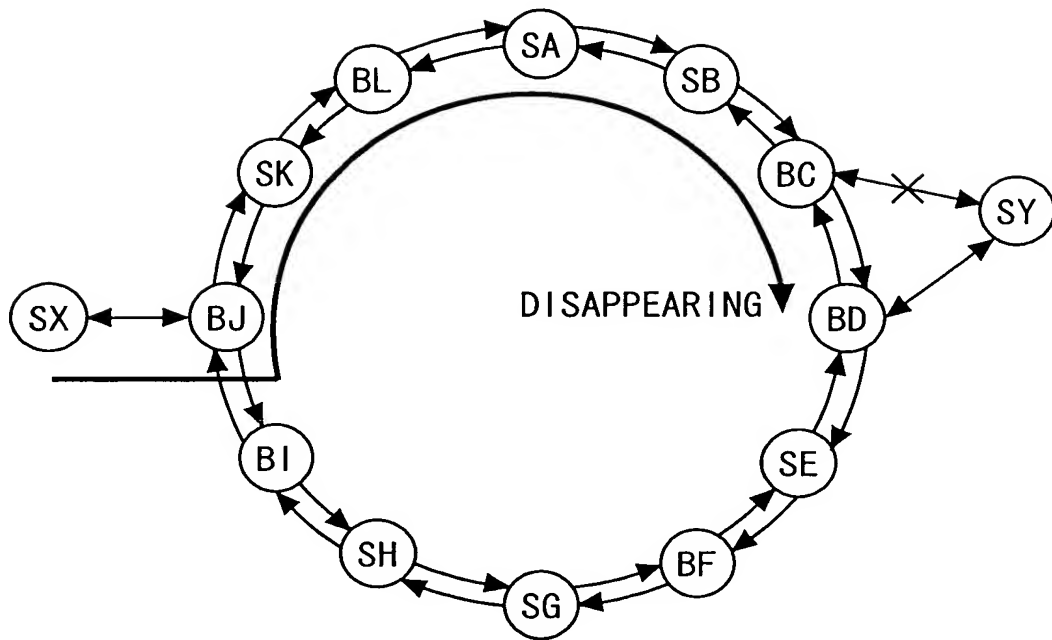


FIG. 28

PRIOR ART

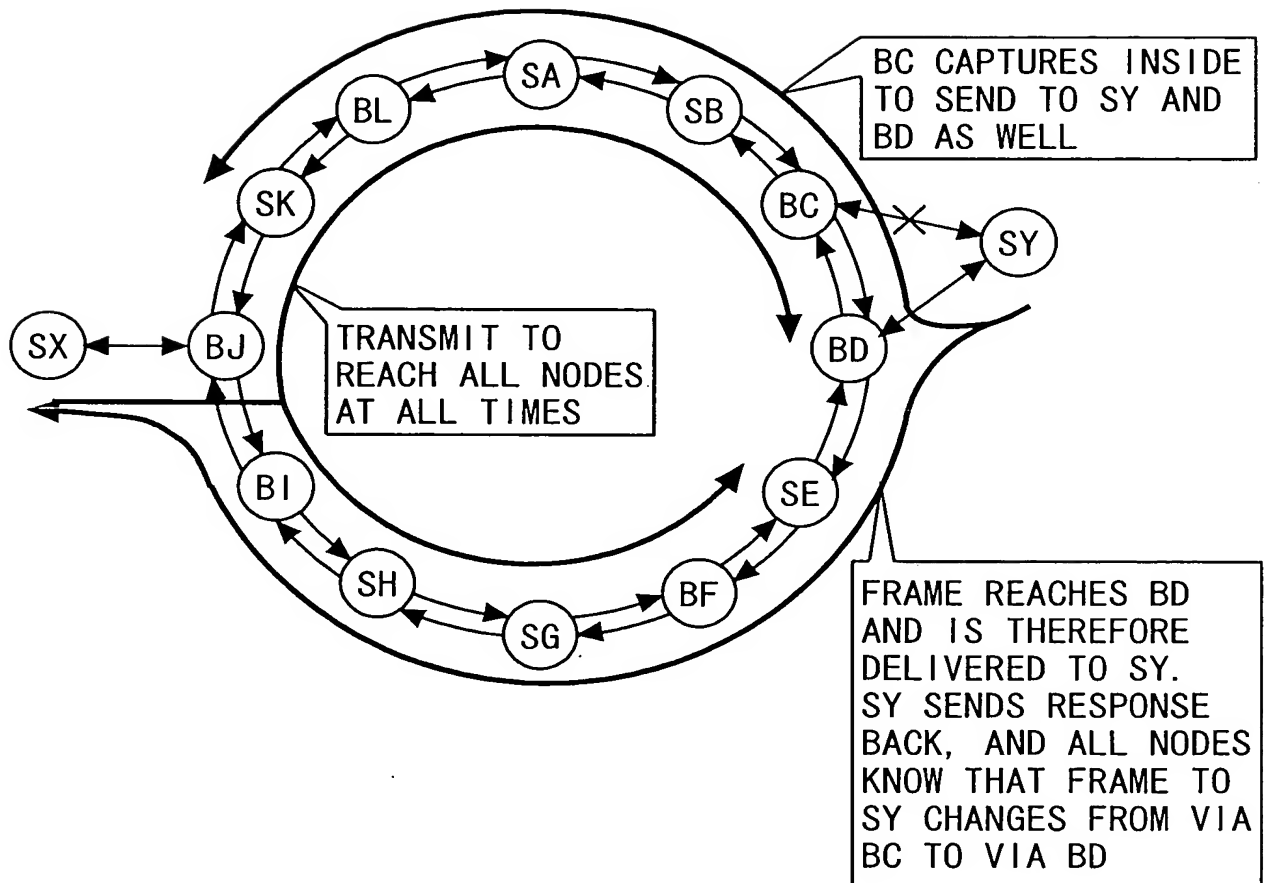


FIG. 29

PRIOR ART

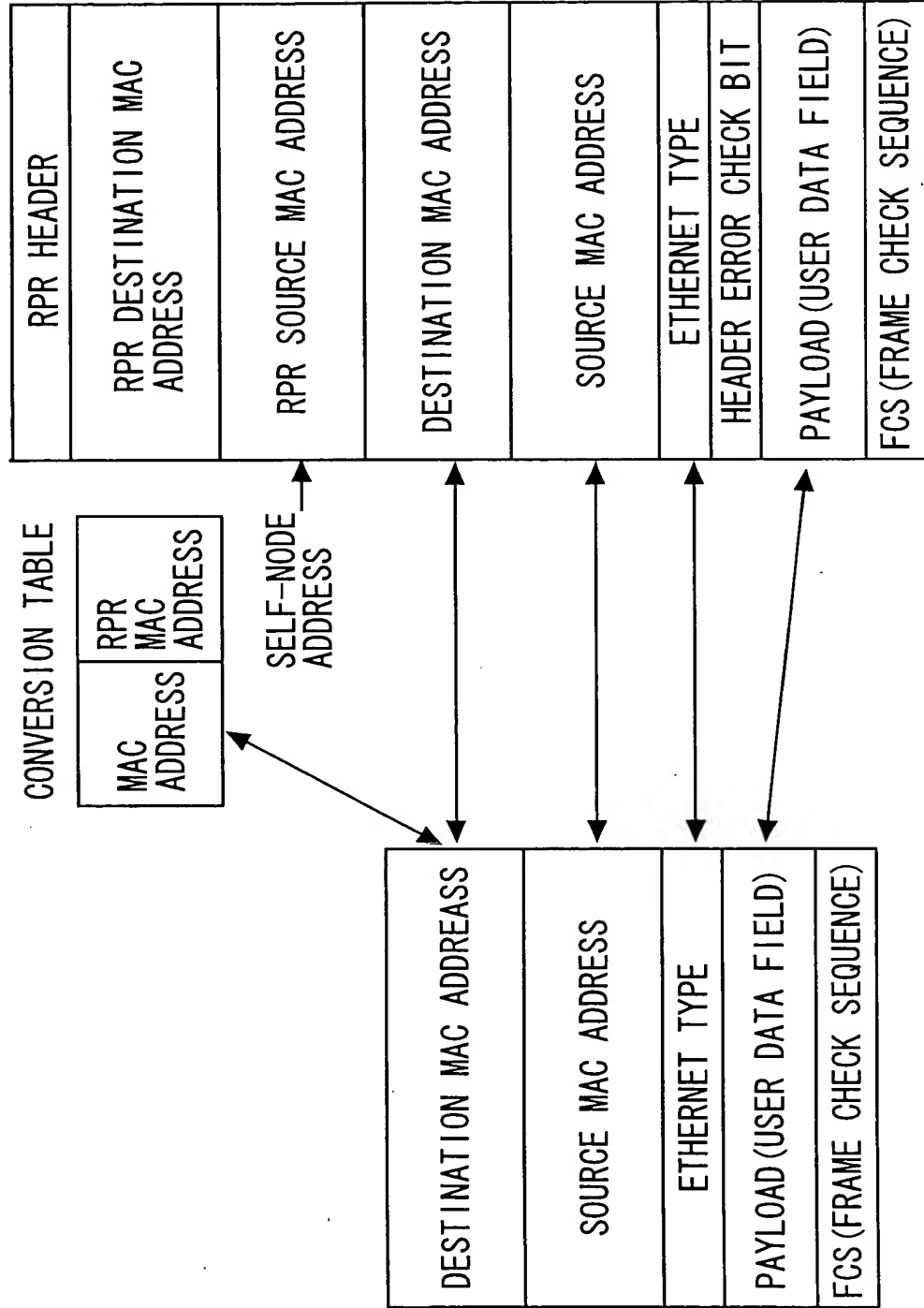


FIG. 30

PRIOR ART

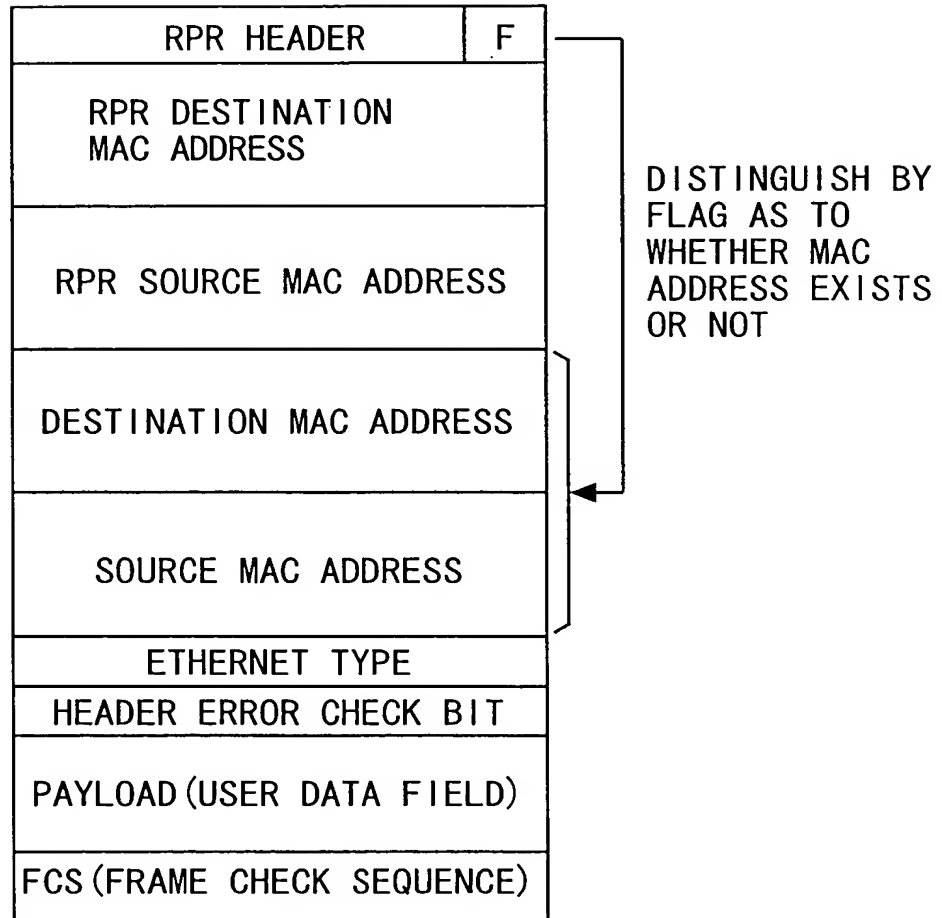


FIG. 31

PRIOR ART

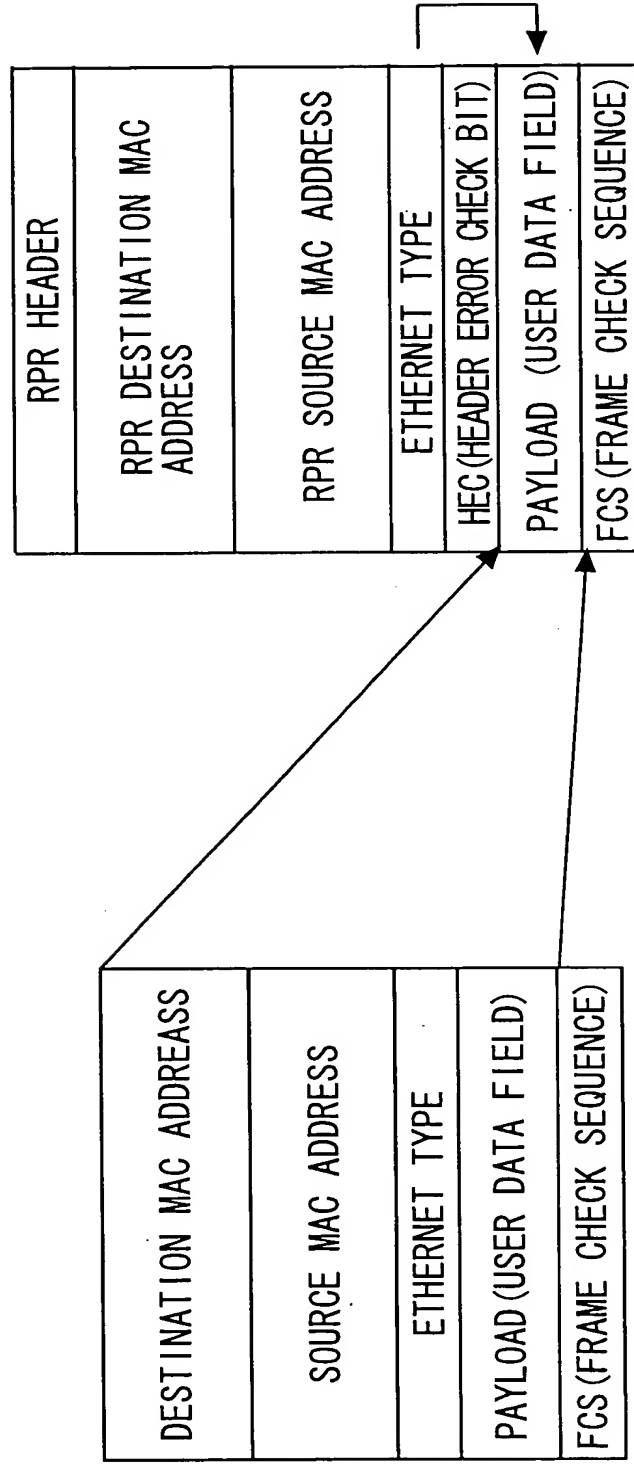


FIG. 32

PRIOR ART

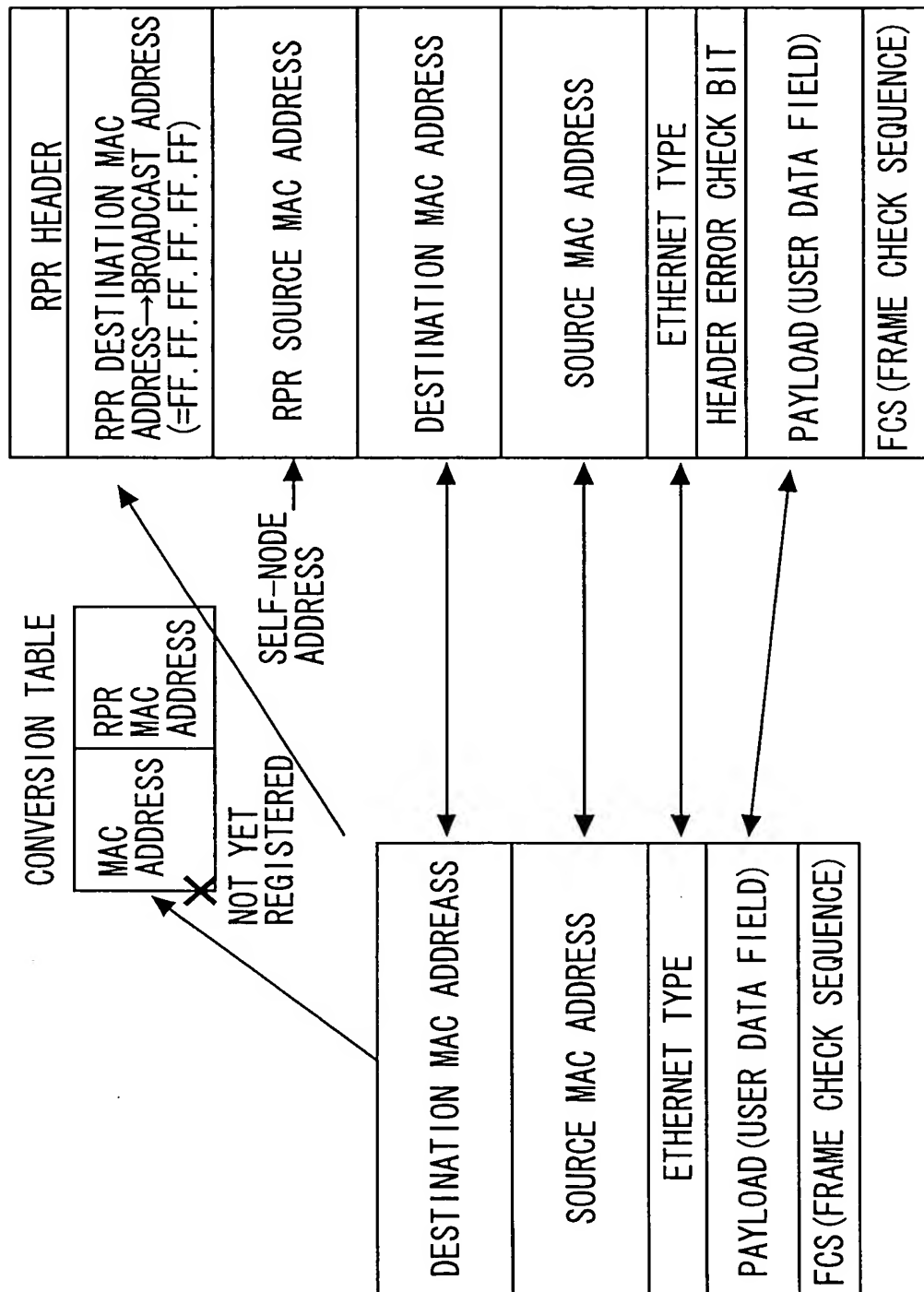


FIG. 33

PRIOR ART

